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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2815

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Dlany
9/16/03

Applicant: Yujiro KAJIHARA *et al.*

Serial No.: 09/989,242

Filed: 21 November 2001

For: LEAD FRAME SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE, USING THE SAME, AND METHOD
OF AND PROCESS FOR FABRICATING THE TWO

Art Unit: 2815

Examiner: J. Clark

Conf. No.: 9603

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AMENDMENT

Commissioner for Patents
POB 1450
Alexandria, Virginia 22313-1450

28 August 2003

Sir:

In response to the Office Action mailed 27 March 2003 in the above-identified application, the following amendments and remarks are respectfully submitted.

In accordance with the revised format of the manner of making amendments under 37 CFR §1.121 as set forth in the Final Rule effective 30 July 2003, each section of amendment herein begins on a new page, and changes are shown by strike-through (or double brackets where appropriate) and underlining to indicate deletions and additions, respectively. A complete listing of all claims ever presented in the application is given with the current status of each claim, and only the text of all pending and withdrawn claims is presented in full, with those not being amended herein being presented in clean version.